| Form PTO 1449 (Modified) | PTO 1449 U.S. DEPARTMENT OF COMME | | COMMERCE MARK OFFICE | ATTY DOCKET NO. 0068-0399-0 | SERIAL NO. 09/420, 524 New Application | | |
|--------------------------|-----------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------|----------------------------------------|-------------------------------------------|-----------------------|-------------------------------|
| LIST OF | REFER | ENCES CITED BY APPI | LICANT | APPLICANT Masakazu KANECHIKA, et al. | | | |
| | | | FILING DATE Herewith | | GROUP 2823 | | |
| | | | 1 | U.S. PATENT DOCUMENTS | | | |
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE |
| | AA | | | | | | |
| | AB | | ļ | | | | |
| | AC | | | | | | |
| | AD | | | | | | |
| | AE | | | | | | |
| | AF | | | | | | |
| | AG | | | | | 1 | |
| | AH | | | | | | |
| | AJ | | | | | | |
| | AK | | | | | | |
| | AL | | | | | | |
| | AM | | | | | | |
| | AN | | | | | | |
| | | | FO | REIGN PATENT DOCUMENTS | | | |
| | | DOCUMENT NUMBER | DATE | COUNTRY | | TRANSLATION YES NO | |
| IAG | AO | 6-163481 | 06/10/94 | JAPAN (with English Abstract) | | | XX |
| JAG | AP | 9-260312 | 10/03/97 | JAPAN (with English Abstract) | | | XX |
| | AQ | | | | | | |
| | AR | | <u> </u> | | | | |
| | AS | | | | | - | |
| | AT | | - | | · | - | |
| | AU | | | | | | |
| | AV | | 1 | <u></u> | | <u> </u> | |
| | ., | | | (Including Author, Title, Date, Pertin | | | OT TRANSICTOR |
| JH | AW | Seigo KANEMARU, et al., "FABRICATION OF METAL-OXIDE-SEMICONDUCTOR FIELD-EFFECT-TRANSISTOR-STRUCTURED SILICON FIELD EMITTERS WITH A POLYSILICON DUAL GATE", Jpn. J. Appl. Phys., Vol. 36, Part 1, No. 12B, December 1997, pages 7736-7740 | | | | | |
| SHG | АХ | Yoshikazu HORI, et al., "NEW SUB-MICORN SIZE SI FIELD EMITTER ARRAYS WITH LOW OPERATION VOLTAGE", Technical Report of The Institute of Electronics Information and Communication Engineers, ED94-95, 1994-12, pages 1-8, (With English Abstract) | | | | | |
| | AY | | | | | | |
| | AZ | | | | | | |
| Examiner | The | anni A. Ma | (| | Date Co | nsidered | July 13, 2002 |

*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.